IGBT with Monolithic Free Wheeling Diode

This Insulated Gate Bipolar Transistor (IGBT) features a robust and cost effective Field Stop (FS) Trench construction, and provides superior performance in demanding switching applications, offering both low on–state voltage and minimal switching loss. The IGBT is well suited for resonant or soft switching applications.

Features

- Extremely Efficient Trench with Fieldstop Technology
- 1350 V Breakdown Voltage
- Optimized for Low Case Temperature in IH Cooker Application
- Reliable and Cost Effective Single Die Solution
- These are Pb-Free Devices

Typical Applications

- Inductive Heating
- Consumer Appliances
- Soft Switching

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Unit	
Collector-emitter voltage	V _{CES}	1350	V	
Collector current @ Tc = 25°C @ Tc = 100°C	I _C	30 15	A	
Pulsed collector current, T _{pulse} limited by T _{Jmax}	I _{CM}	60	Α	
Diode forward current @ Tc = 25°C @ Tc = 100°C	l _F	30 15	А	
Diode pulsed current, T _{pulse} limited by T _{Jmax}	I _{FM}	60	Α	
Gate-emitter voltage Transient Gate-emitter Voltage $(T_{pulse} = 5 \mu s, D < 0.10)$	V_{GE}	±20 ±25	V	
Power Dissipation @ Tc = 25°C @ Tc = 100°C	P _D	357 178	W	
Operating junction temperature range	TJ	-40 to +175	°C	
Storage temperature range	T _{stg}	-55 to +175	°C	
Lead temperature for soldering, 1/8" from case for 5 seconds	T _{SLD}	260	°C	

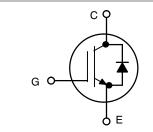
Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

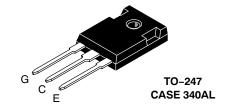


ON Semiconductor®

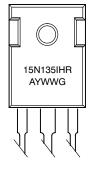
http://onsemi.com

15 A, 1350 V V_{CEsat} = 2.15 V E_{off} = 0.42 mJ





MARKING DIAGRAM



A = Assembly Location

Y = Year WW = Work Week G = Pb-Free Package

ORDERING INFORMATION

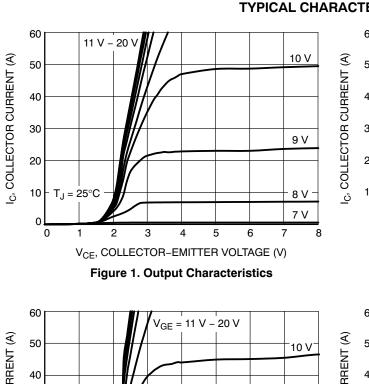
Device	Package	Shipping
NGTB15N135IHRWG	TO-247 (Pb-Free)	30 Units / Rail

THERMAL CHARACTERISTICS

Rating	Symbol	Value	Unit
Thermal resistance junction-to-case	$R_{ heta JC}$	0.42	°C/W
Thermal resistance junction-to-ambient	$R_{ hetaJA}$	40	°C/W

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise specified)

Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
STATIC CHARACTERISTIC						•
Collector-emitter breakdown voltage, gate-emitter short-circuited	$V_{GE} = 0 \text{ V}, I_{C} = 500 \mu\text{A}$	V _{(BR)CES}	1350	_	_	V
Collector-emitter saturation voltage	V _{GE} = 15 V, I _C = 15 A V _{GE} = 15 V, I _C = 15 A, T _J = 175°C	V _{CEsat}	- -	2.15 2.25	2.65 -	V
Gate-emitter threshold voltage	$V_{GE} = V_{CE}, I_{C} = 250 \mu A$	V _{GE(th)}	4.5	5.5	6.5	٧
Collector-emitter cut-off current, gate- emitter short-circuited	V _{GE} = 0 V, V _{CE} = 1350 V	I _{CES}	-	-	0.1	mA
Gate leakage current, collector-emitter short-circuited	V _{GE} = 20 V, V _{CE} = 0 V	I _{GES}	-	-	100	nA
DYNAMIC CHARACTERISTIC		•		•		
Input capacitance	V _{CE} = 20 V, V _{GE} = 0 V, f = 10 kHz	C _{ies}	-	3560	_	pF
Output capacitance		C _{oes}	-	87	-	
Reverse transfer capacitance		C _{res}	-	68	-	
Gate charge total	V _{CE} = 600 V, I _C = 15 A, V _{GE} = 15 V	Qg	-	156	-	nC
Gate to emitter charge		Q _{ge}	-	27	-	
Gate to collector charge		Q _{gc}	_	70	_	
SWITCHING CHARACTERISTIC, INDUCT	TIVE LOAD		-	2	-	
Turn-off delay time	$T_J = 25^{\circ}C$ $V_{CC} = 600 \text{ V, } I_C = 15 \text{ A}$ $R_g = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{ V}$	t _{d(off)}	_	170	_	ns
Fall time		t _f	-	200	-	
Turn-off switching loss		E _{off}	-	0.42	-	mJ
Turn-off delay time	$T_J = 150^{\circ}\text{C}$ $V_{CC} = 600 \text{ V, } I_C = 15 \text{ A}$ $R_0 = 10 \Omega$	t _{d(off)}	-	190	-	ns
Fall time		t _f	-	290	-	
Turn-off switching loss	$R_g = 10 \Omega$ $V_{GE} = 0 \text{ V/ } 15 \text{V}$	E _{off}	-	0.95	-	mJ
DIODE CHARACTERISTIC						
Forward voltage	V _{GE} = 0 V, I _F = 15 A, T _J = 25°C V _{GE} = 0 V, I _F = 15 A, T _J = 175°C	V _F	_ _	1.85 2.75	2.10 -	V



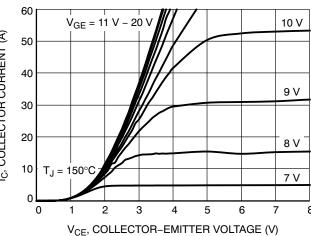
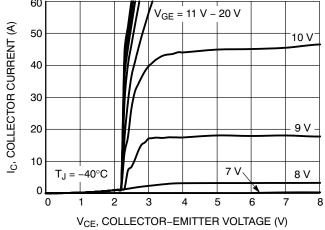


Figure 2. Output Characteristics



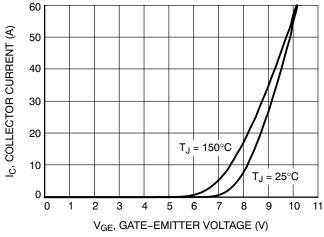
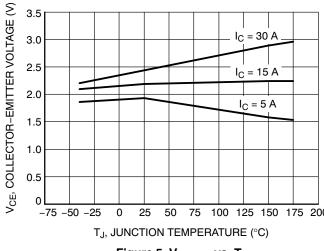


Figure 3. Output Characteristics





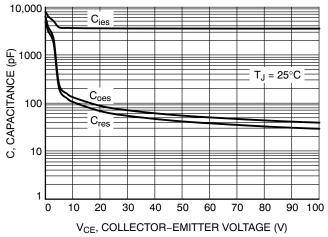
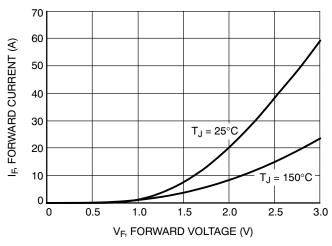


Figure 5. V_{CE(sat)} vs. T_J

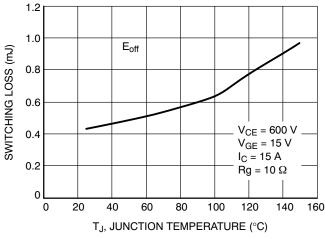
Figure 6. Typical Capacitance



16 V_{GE}, GATE-EMITTER VOLTAGE (V) 14 12 10 8 6 V_{CE} = 600 V V_{GE} = 15 V $I_{C} = 15 A$ 2 0 40 20 60 80 100 120 140 160 180 200 0 Q_G, GATE CHARGE (nC)

Figure 7. Diode Forward Characteristics

Figure 8. Typical Gate Charge



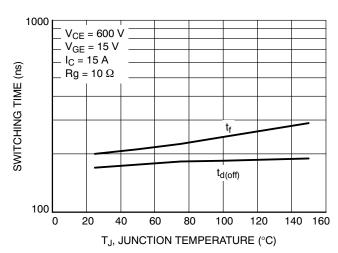
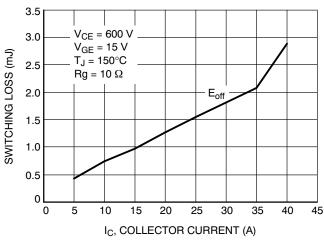


Figure 9. Switching Loss vs. Temperature

Figure 10. Switching Time vs. Temperature



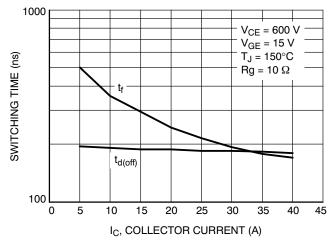
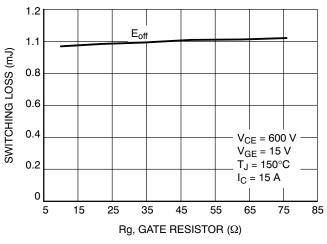


Figure 11. Switching Loss vs. I_C

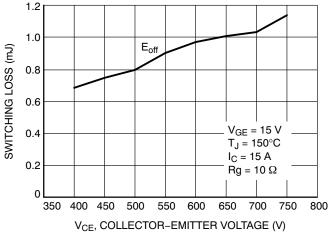
Figure 12. Switching Time vs. I_C



1000 t_{d(off)} SWITCHING TIME (ns) t_f $V_{CE} = 600 \text{ V}$ $V_{GE} = 15 V$ $T_{J} = 150^{\circ}C$ I_C = 15 A 100 25 15 35 45 55 65 75 85 Rg, GATE RESISTOR (Ω)

Figure 13. Switching Loss vs. Rg

Figure 14. Switching Time vs. Rg



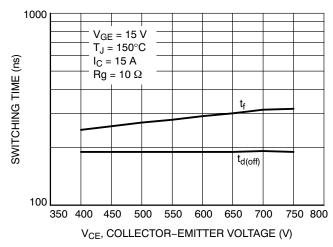
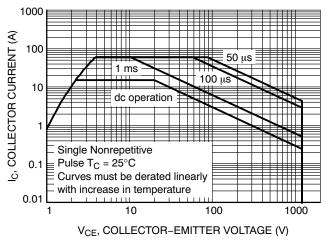


Figure 15. Switching Loss vs. V_{CE}

Figure 16. Switching Time vs. V_{CE}



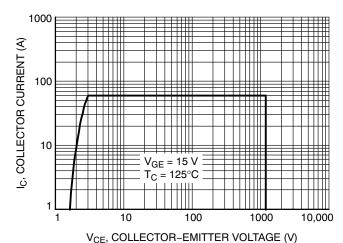


Figure 17. Safe Operating Area

Figure 18. I_C vs. V_{CE}

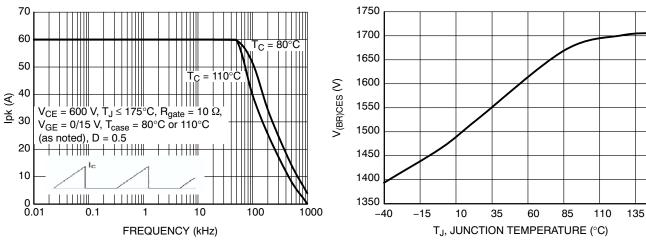


Figure 19. Collector Current vs. Switching Frequency

Figure 20. Typical $V_{(BR)CES}$ vs. Temperature

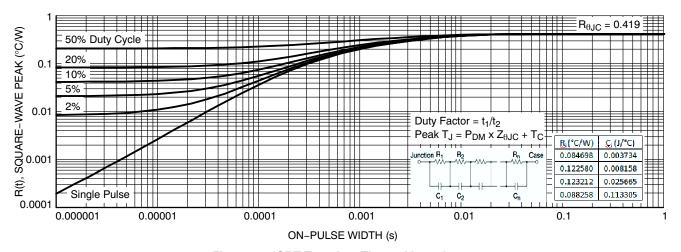


Figure 21. IGBT Transient Thermal Impedance

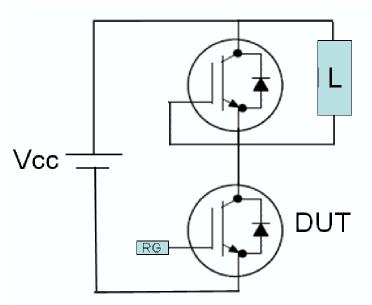


Figure 22. Test Circuit for Switching Characteristics

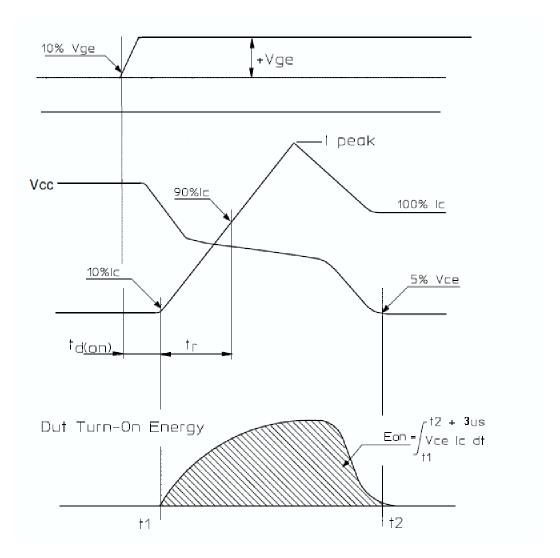


Figure 23. Definition of Turn On Waveform

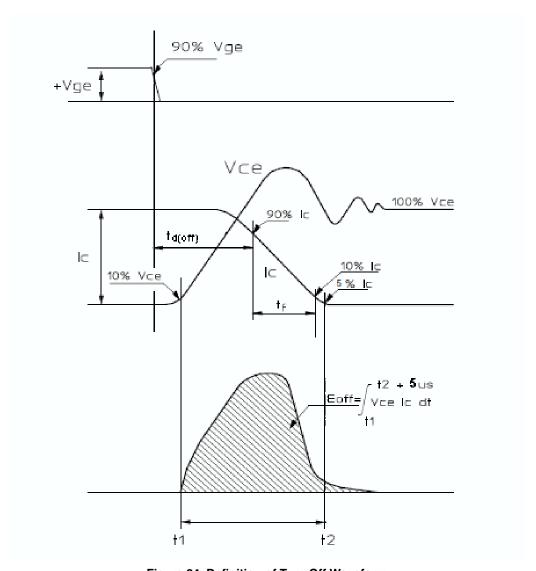
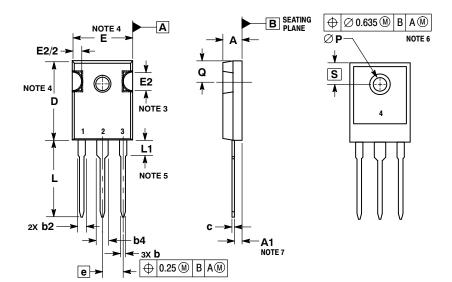


Figure 24. Definition of Turn Off Waveform

PACKAGE DIMENSIONS

TO-247 CASE 340AL ISSUE A



NOTES

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M. 1994.
- CONTROLLING DIMENSION: MILLIMETERS.
 SLOT REQUIRED, NOTCH MAY BE ROUNDED.
- DIMENSIONS D AND E DO NOT INCLUDE MOLD FLASH. MOLD FLASH SHALL NOT EXCEED 0.13 PER SIDE. THESE DIMENSIONS ARE MEASURED AT THE OUTERMOST EXTREME OF THE PLASTIC BODY.
- LEAD FINISH IS UNCONTROLLED IN THE REGION DEFINED BY
- ØP SHALL HAVE A MAXIMUM DRAFT ANGLE OF 1.5° TO THE
- TOP OF THE PART WITH A MAXIMUM DIAMETER OF 3.91. DIMENSION A1 TO BE MEASURED IN THE REGION DEFINED

	MILLIMETERS		
DIM	MIN	MAX	
Α	4.70	5.30	
A1	2.20	2.60	
b	1.00	1.40	
b2	1.65	2.35	
b4	2.60	3.40	
С	0.40	0.80	
D	20.30	21.40	
E	15.50	16.25	
E2	4.32	5.49	
е	5.45 BSC		
L	19.80	20.80	
L1	3.50	4.50	
P	3.55	3.65	
Q	5.40	6.20	
S	6.15 BSC		

ON Semiconductor and war registered trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of SCILLC's product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any licenses under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body or other applications intended to sungert or sustain life or for any other application in which the failure of the SCILLC product could co surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

PUBLICATION ORDERING INFORMATION

LITERATURE FULFILLMENT:

Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA

Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: orderlit@onsemi.com

N. American Technical Support: 800-282-9855 Toll Free USA/Canada Europe, Middle East and Africa Technical Support: Phone: 421 33 790 2910 Japan Customer Focus Center

Phone: 81-3-5817-1050

ON Semiconductor Website: www.onsemi.com

Order Literature: http://www.onsemi.com/orderlit

For additional information, please contact your local Sales Representative